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WHAT IS CLAIMED IS:

A cell switch comprising:

a first switch plane comprising a first switch core;

a second switch plane comprising a second switch core;

a sender switch port interface unit;

a receiver switch port interface unit;

wherein the sender switch port interface unit sends maintenance cells to the receiver switch port interface unit via the first switch plane and the second switch plane, the maintenance cells being applied to the first switch plane and the second switch plane in a predetermined sequence;

wherein the receiver switch port interface unit detects an erroneous switch plane when the maintenance cells from the sender switch port interface unit do not arrive in the predetermined sequence.

- 2. The apparatus of claim 1, wherein the predetermined sequence comprises maintenance cells applied to the first witch plane and the second switch plane in alternation.
- 3. The apparatus of claim 1, wherein the maintenance cell includes a plane indicator which informs the receiver switch port interface unit whether the maintenance cell traveled through the first switch plane or the second switch plane.
- 4. The apparatus of claim 1, wherein the sender switch port interface unit sends a cycle of maintenance cells to the receiver switch port interface unit, the cycle comprising plural sets of the predetermined sequence, and wherein at least a portion of the maintenance cell of one of the sets of the cycle is inverted with respect to a corresponding portion of the maintenance cell of another of the sets of the cycle.
- 5. The apparatus of claim 4, wherein the cycle comprises two sets of the predetermined sequence of maintenance cells.

6. The apparatus of claim 1, wherein the receiver switch port interface unit detects an erroneous switch plane by performing error checking with respect to contents of a received maintenance cell.

- 7. The apparatus of claim 1, further comprising N number of receiver switch port interface units, and wherein the sender switch port interface unit applies maintenance cells to the N number of receiver switch port interface units in a cycle, the cycle comprising (1) non-inverted maintenance cells sent via the first switch plane to each of the N number of receiver switch port interface units; (2) non-inverted maintenance cells sent via the second switch plane to each of the N number of receiver switch port interface units; (3) inverted maintenance cells sent via the first switch plane to each of the N number of receiver switch port interface units; and (4) inverted maintenance cells sent via the second switch plane to each of the N number of receiver switch port interface units, the inverted maintenance cells having at least a portion thereof inverted with respect to the corresponding non-inverted cell of the cycle.
 - 8. A cell switch comprising: a switch plane comprising a switch core; a sender switch port interface unit; a receiver switch port interface unit;

wherein the sender switch port interface unit sends a pair of maintenance cells to the receiver switch port interface unit via the switch plane, a first maintenance cell of the pair having a predetermined portion thereof inverted with respect to a corresponding portion of a second maintenance cell of the pair.

9. The apparatus of claim 8, further comprising a second switch plane through which the sender switch port interface unit also sends a second pair of maintenance cells to the receiver switch port interface unit, a first maintenance cell of the second pair having a predetermined portion thereof inverted with respect to a corresponding portion of a second maintenance cell of the second pair.

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10.	Α	cell	switch	comprising:
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- a first switch plane comprising a first switch core;
- a second switch plane comprising a second switch core;
- a sender switch port interface unit;

plural receiver switch port interface units, and wherein the sender switch port interface unit applies maintenance cells to the plural receiver switch port interface units in a cycle, the cycle comprising (1) non-inverted maintenance cells sent via the first switch plane to each of the plural receiver switch port interface units; (2) non-inverted maintenance cells sent via the second switch plane to each of the plural receiver switch port interface units; (3) inverted maintenance cells sent via the first switch plane to each of the plural receiver switch port interface units; and (4) inverted maintenance cells sent via the second switch plane to each of the plural receiver switch port interface units, the inverted maintenance cells having at least a portion thereof inverted with respect to the corresponding non-inverted cell of the cycle.

- 11. A method of operating a cell switch comprising:
- a first switch plane comprising a first switch core;
- a second switch plane comprising a second switch core;
- a sender switch port interface unit;
- a receiver switch port interface unit;

a sender switch port interface unit sending maintenance cells to a receiver switch port interface unit via a first switch plane and a second switch plane, the maintenance cells being applied to the first switch plane and the second switch plane in a predetermined sequence;

detecting, at the receiver switch port interface unit, an erroneous switch plane when the maintenance cells from the sender switch port interface unit do not arrive in the predetermined sequence.

12. The method of claim 11, further comprising forming the predetermined sequence by alternating application of the maintenance cells to the first switch plane and to the second switch plane.

- 18. The method of claim 11, further comprising including in the maintenance cell a plane indicator and using the plane indicator at the receiver switch port interface unit to determine whether the maintenance cell traveled through the first switch plane or the second switch plane.
- 14. The method of claim 11, further comprising sending a cycle of maintenance cells to the receiver switch port interface unit, the cycle comprising plural sets of the predetermined sequence, and wherein at least a portion of the maintenance cell of one of the sets of the cycle is inverted with respect to a corresponding portion of the maintenance cell of another of the sets of the cycle.
- 15. The method of claim 14, further comprising forming the cycle with two sets of the predetermined sequence of maintenance cells.
- 16. The method of claim 11, further comprising detecting at the receiver switch port interface unit an errone ous switch plane by performing error checking with respect to contents of a received maintenance cell.
- 17. The method of claim 11, further comprising the sender switch port interface unit applying maintenance cells to N number of receiver switch port interface units in a cycle, the cycle comprising (1) non-inverted maintenance cells sent via the first switch plane to each of the N number of receiver switch port interface units; (2) non-inverted maintenance cells sent via the second switch plane to each of the N number of receiver switch port interface units; (3) inverted maintenance cells sent via the first switch plane to each of the N number of receiver switch port interface units; and (4) inverted maintenance cells sent via the second switch plane to each of the N number of receiver switch port interface units, the inverted maintenance cells having at least a portion thereof inverted with respect to the corresponding non-inverted cell of the cycle.
- 18. A method of operating a cell switch comprising sending a pair of maintenance cells from a sender switch port interface unit to a receiver switch port interface unit via a switch plane, a first maintenance cell of the pair having a predetermined portion thereof inverted with respect to a corresponding portion of a second maintenance cell of the pair.

- 19. The method of claim 18, further comprising sending a second pair of maintenance cells via a second switch plane to the receiver switch port interface unit, a first maintenance cell of the second pair having a predetermined portion thereof inverted with respect to a corresponding portion of a second maintenance cell of the second pair.
- 20. A method of operating a cell switch comprising applying maintenance cells from a sender switch port interface unit to plural receiver switch port interface units in a cycle, the cycle comprising (1) non-inverted maintenance cells sent via a first switch plane to each of the plural receiver switch port interface units; (2) non-inverted maintenance cells sent via a second switch plane to each of the plural receiver switch port interface units; (3) inverted maintenance cells sent via the first switch plane to each of the plural receiver switch port interface units; and (4) inverted maintenance cells sent via the second switch plane to each of the plural receiver switch port interface units, the inverted maintenance cells having at least a portion thereof inverted with respect to the corresponding non-inverted cell of the cycle.